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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/420,635
Filing Date October 21, 1999
Inventor Werner Juengling
Assignee Micron Technology, Inc.
Group Art Unit 2812
Examiner H. Tsai
Attorney's Docket No. MI22-1243
Title: Semiconductor Processing Methods of Forming Devices on a Substrate,
Forming Device Arrays on a Substrate, Forming Conductive Lines on a
Substrate, and Forming Capacitor Arrays on a Substrate, and
Integrated Circuitry

RESPONSE TO JUNE 14, 2002 FINAL OFFICE ACTION
RESPONSE TO ACCOMPANY RCE FILING

To: Box RCE
Assistant Commissioner for Patents
Washington, D.C. 20231

From: Frederick M. Fliegel, Ph.D.
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TECHNOLOGY CENTER 2800

Responsive to the Final Office Action dated June 14, 2002, Applicant
remarks as follows:

AMENDMENTS

D